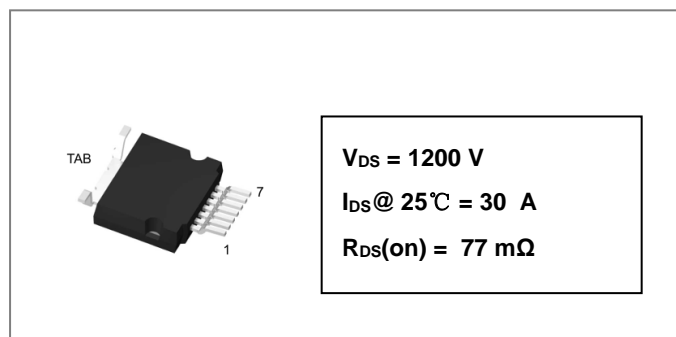


S2M0080120B

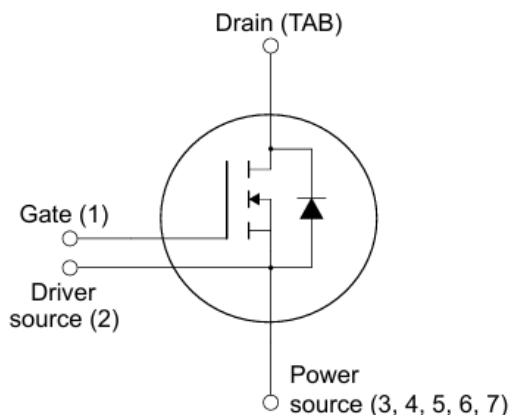
1200V SiC POWER MOSFET



Description

S2M0080120B is a single SiC Power MOSFET packaged in T2PAK case. The device is a high voltage n-channel Enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0080120B is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Features

- Positive temperature characteristics, easy to parallel.
 - Low on-resistance typ. $R_{DS(on)} = 77\text{ m}\Omega$.
 - Fast switching speed and low switching losses.
 - Very fast and robust intrinsic body diode.
 - Process of non-bright tin electroplatin.
- “-A” is an AEC-Q101 qualified device.

Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

Maximum Ratings ($T_A = 25^\circ\text{C}$, unless otherwise specified)

Characteristics	Symbol	Conditions	Max.	Units
Drain Source Voltage	V_{DSS}	$V_{GS} = 0V, I_{DS} = 100\mu A, T_C = 25^\circ\text{C}$	1200	V
Gate Source Voltage	V_{GSS}	$T_C = 25^\circ\text{C}$, Absolute maximum values, AC ($f > 1\text{Hz}$)	-10 to +25	V
Gate Source Voltage	V_{GSOP}	$T_C = 25^\circ\text{C}$ Recommended Operational Values	-5 to +20	V
Continuous Drain Current	I_D	$V_{GS} = 20V, T_C = 25^\circ\text{C}$	30	A
	I_D	$V_{GS} = 20V, T_C = 100^\circ\text{C}$	21	A
Pulsed Drain Current	$I_{D,pulse}$	$T_C = 25^\circ\text{C}$	82	A
Power Dissipation	P_D	$T_C = 25^\circ\text{C}$	176	W

[1] Recommended turn off gate voltage is -5 V. Recommended turn on gate voltage is 20 V. Do not use with $V_{GSON} < 15\text{ V}$.

Electrical Characteristics ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Typ.	Max.	Units
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	1200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 10mA$	2.0	2.8	4.0	V
		$V_{DS} = V_{GS}, I_D = 10mA, T_J = 175\text{ }^{\circ}\text{C}$		1.8		V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 1200V, V_{GS} = 0V$		0.1	1.0	μA
	I_{DSS}	$V_{DS} = 1200V, V_{GS} = 0V, T_J = 175\text{ }^{\circ}\text{C}$		1		μA
Gate Source Leakage Current	I_{GSS+}	$V_{GS} = 20V, V_{DS} = 0V$		10	100	nA
	I_{GSS-}	$V_{GS} = -5V, V_{DS} = 0V$		-10	-100	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 20V, I_D = 20A$		77	100	m Ω
		$V_{GS} = 20V, I_D = 20A, T_J = 175\text{ }^{\circ}\text{C}$		137		m Ω
Transconductance	gfs	$V_{DS} = 20V, I_D = 20A$		10.5		S
		$V_{DS} = 20V, I_D = 20A, T_J = 175\text{ }^{\circ}\text{C}$		8		S
Input Capacitance	C_{ISS}	$V_{GS} = 0V$		1324		pF
Output Capacitance	C_{OSS}	$V_{DS} = 1000V$		74		
Reverse Transfer Capacitance	C_{RSS}	$V_{AC} = 25\text{ mV}$		3.4		
Coss Stored Energy	E_{OSS}	$f = 200kHz$		37		μJ
Turn-On Switching Energy	E_{ON}	$V_{DS} = 800V, V_{GS} = -5/20V$		290		μJ
Turn-Off Switching Energy	E_{OFF}	$I_D = 20A, R_{G(ext)} = 2.5\text{ }\Omega$		20		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800V, V_{GS} = -5/20V$		20		ns

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RoHS

Rise Time	t_r	ID = 20 A, RG(ext) = 2.5 Ω , L=975uH, FWD=S2M0080120K		11		
Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to		20		
Fall Time	t_f	VDS Per IEC60747-8-4 pg 83		7.8		
Internal Gate Resistance	$R_{G(int)}$	f = 1 MHz, AC = 25 mV		3.3		Ω
Gate to Source Charge	Q_{gs}	$V_{DS} = 800$ V, $V_{GS} = -5/20$ V		23		nC
Gate to Drain Charge	Q_{gd}	$I_D = 20$ A		14		
Total Gate Charge	Q_g	Per IEC60747-8-4 pg 21		54		

Reverse Diode Characteristics ($T_A = 25^\circ\text{C}$, unless otherwise specified)

Characteristics	Symbol	Conditions	Typ.	Max.	Units
Diode Forward Voltage	V_{SD}	$V_{GS} = -5\text{V}$, $I_{SD} = 10\text{A}$	4.0		V
	V_{SD}	$V_{GS} = -5\text{V}$, $I_{SD} = 10\text{A}$, $T_J = 175^\circ\text{C}$	3.5		V
Continuous Diode Forward Current	I_S	$V_{GS} = -5\text{V}$, $T_C = 25^\circ\text{C}$	41		A
Reverse Recovery Time	t_{rr}	$V_{GS} = -5$ V, $I_{SD} = 20$ A, $T_J = 25^\circ\text{C}$	25		ns
Reverse Recovery Charge	Q_{rr}	$V_R = 800\text{V}$	102		nC
Peak Reverse Recovery Current	I_{mm}	$di/dt = 1950$ A / μs	6.7		A

Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T_J	-	-55 to +175	°C
Storage Temperature	T_{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	$R_{\theta JC}$	DC operation	0.85	°C/W

Ordering Information

Device	Package	Shipping
S2M0080120B	T2PAK	700pcs/reel
S2M0080120BTR	T2PAK	700pcs/reel

Marking Diagram



Where XXXXX is YYWWL

S2M = Device Type
 0080 = $R_{DS(on)}$
 120 = Reverse Voltage (1200V)
 B = Package
 SSG = SSG
 YY = Year
 WW = Week
 L = Lot Number

Cautions: Molding resin
 Epoxy resin UL:94V-0

Ratings and Characteristics Curves

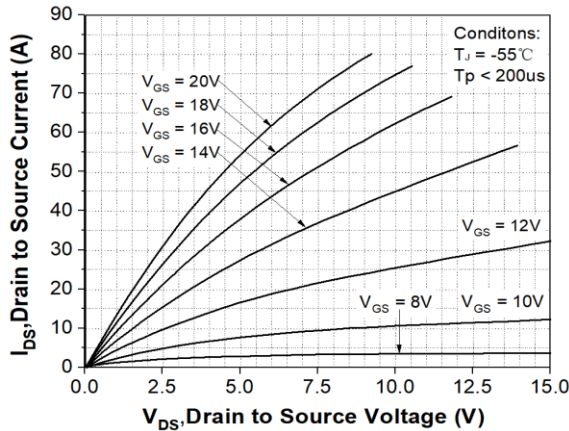


Figure 1. Output Characteristics $T_J = -55^\circ\text{C}$

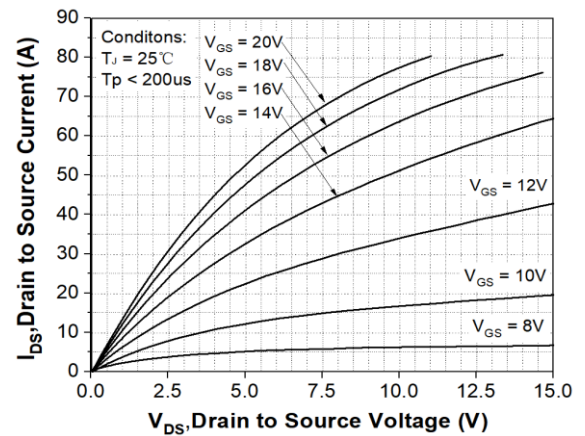


Figure 2. Output Characteristics $T_J = 25^\circ\text{C}$

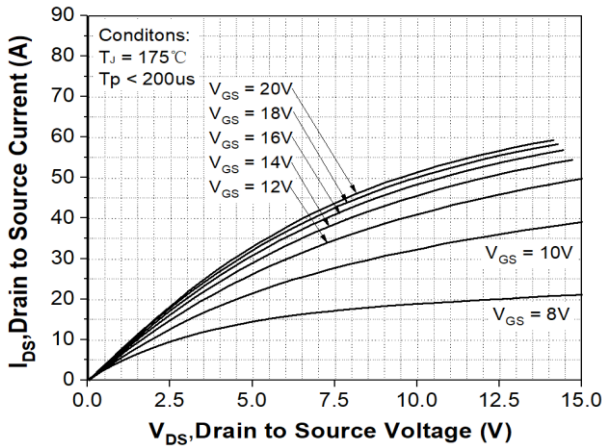


Figure 3. Output Characteristics $T_J = 175^\circ\text{C}$

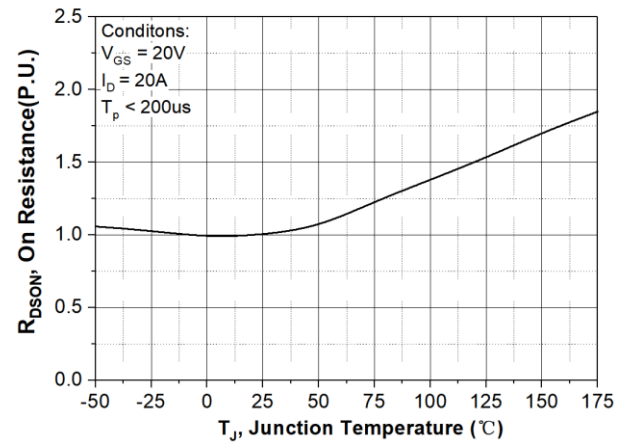


Figure 4. Normalized On-Resistance vs. Temperature

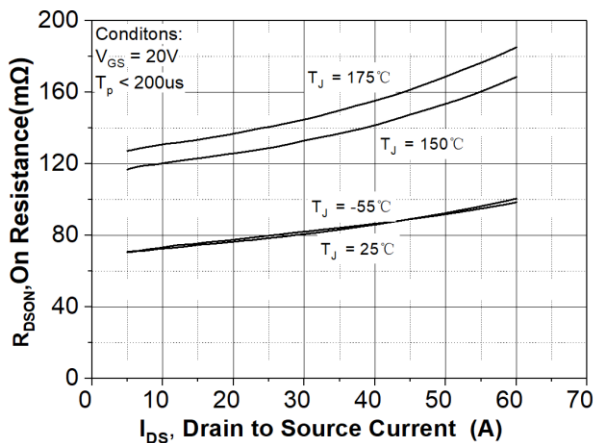


Figure 5. On-Resistance vs. Drain Current
For Various Temperatures

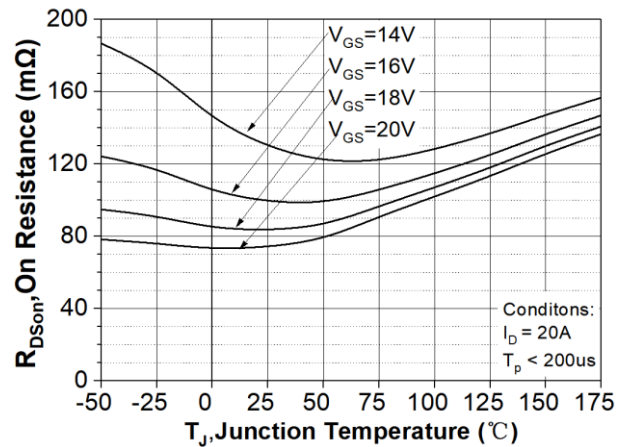


Figure 6. On-Resistance vs. Temperature
For Various Gate Voltage

Technical Data
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RoHS

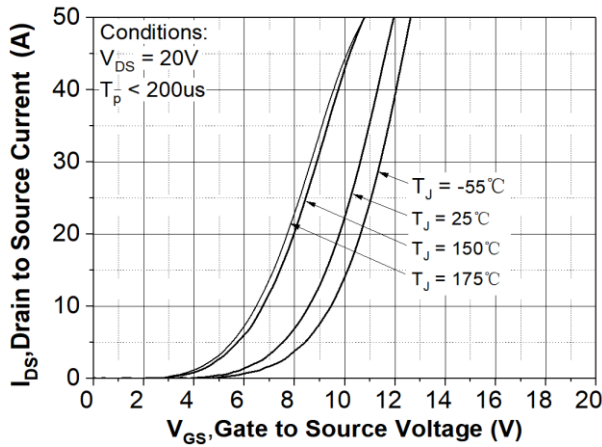


Figure 7. Transfer Characteristic for Various Junction Temperatures

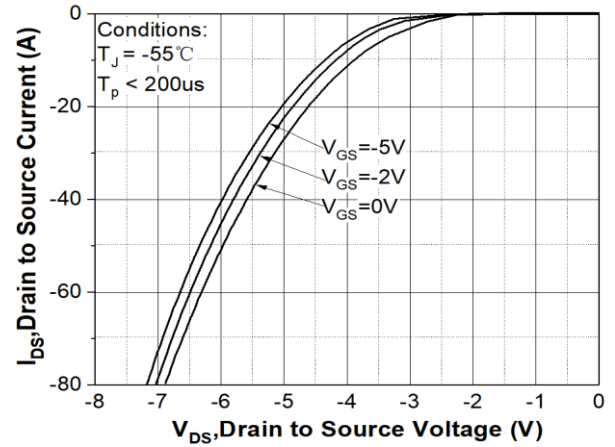


Figure 8. Body Diode Characteristic at $T_J = -55\text{ }^{\circ}\text{C}$

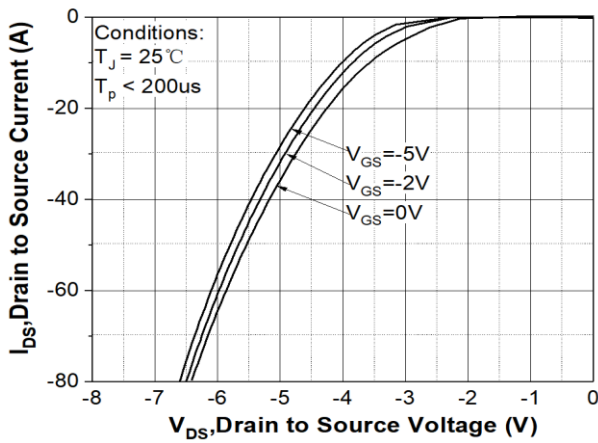


Figure 9. Body Diode Characteristic at $T_J = 25\text{ }^{\circ}\text{C}$

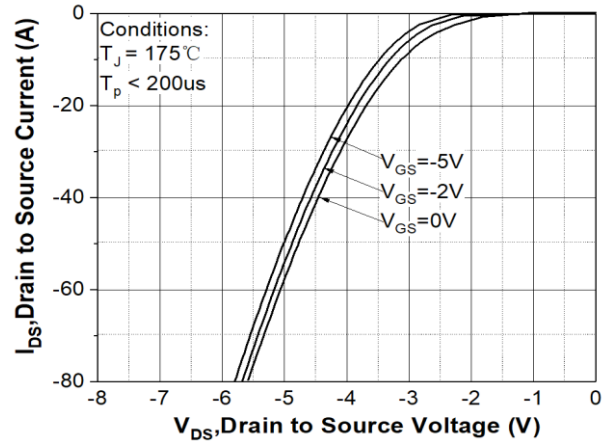


Figure 10. Body Diode Characteristic at $T_J = 175\text{ }^{\circ}\text{C}$

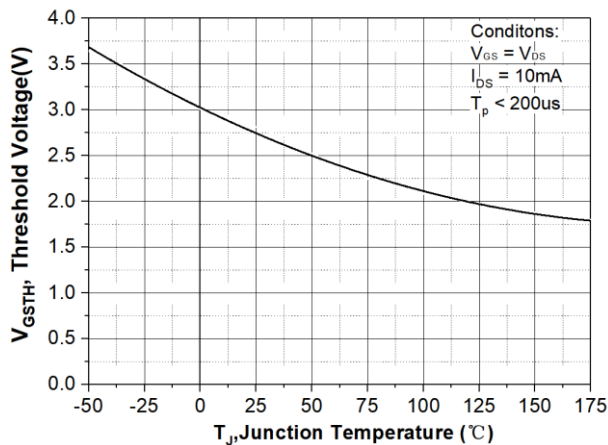


Figure 11. Threshold Voltage vs. Temperature

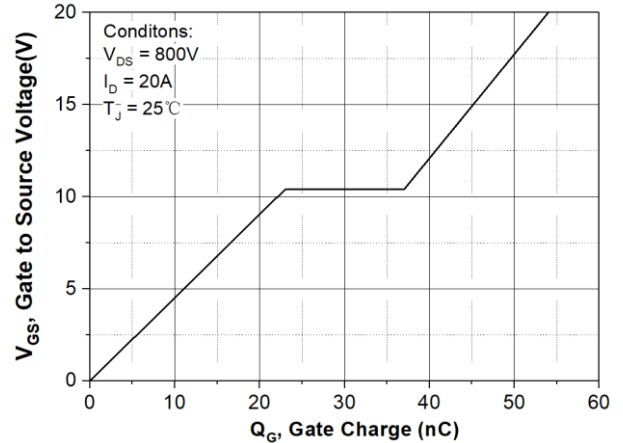


Figure 12. Gate Charge Characteristic

Technical Data
Data Sheet N2835, REV.-

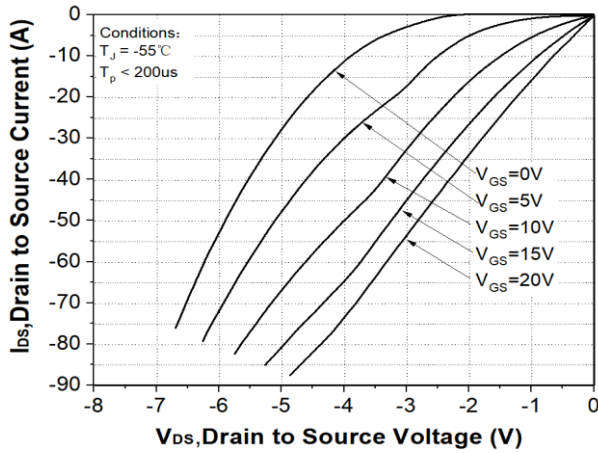


Figure 13. 3rd Quadrant Characteristic at $T_J = -55^\circ\text{C}$

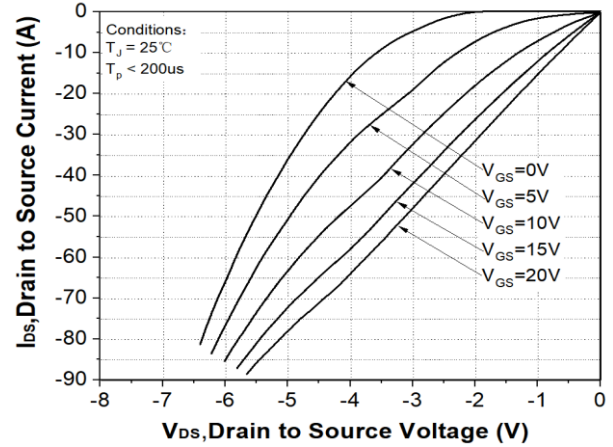


Figure 14. 3rd Quadrant Characteristic at $T_J = 25^\circ\text{C}$

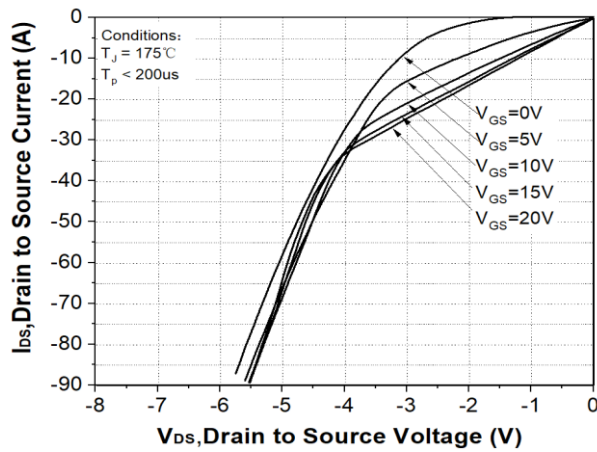


Figure 15. 3rd Quadrant Characteristic at $T_J = 175^\circ\text{C}$

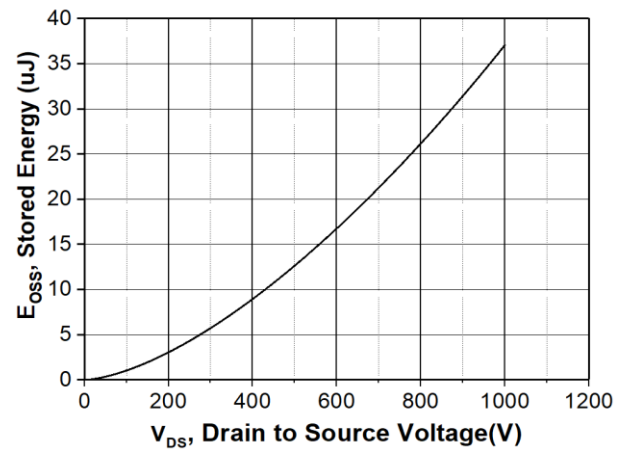


Figure 16. Output Capacitor Stored Energy

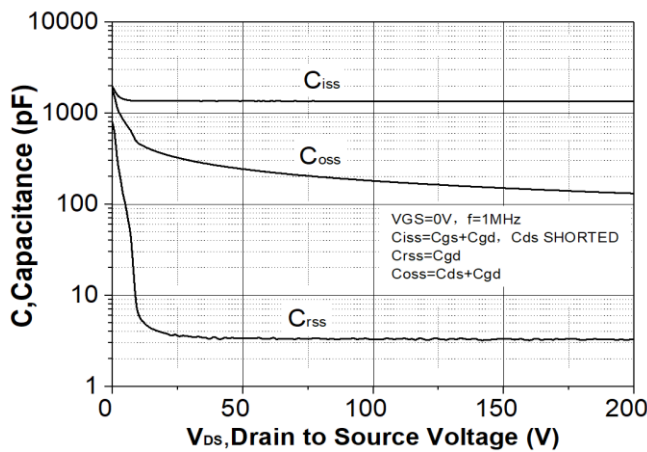


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200 V)

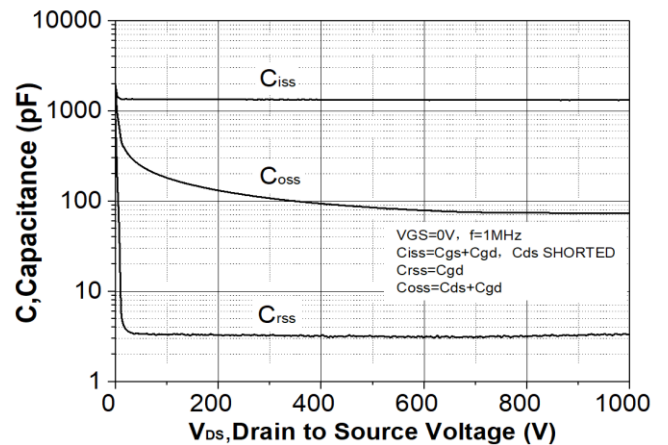


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000 V)

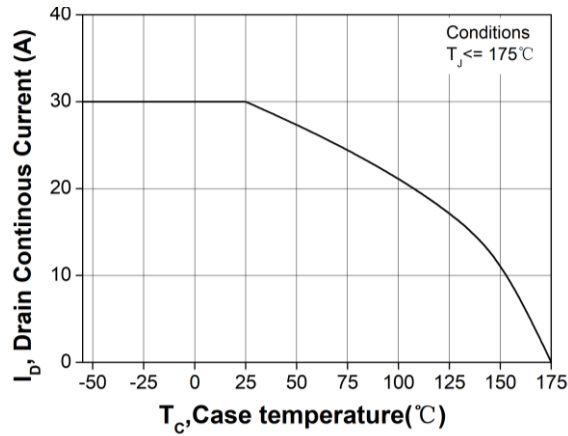


Figure 19. Continuous Drain Current Derating vs. Case Temperature

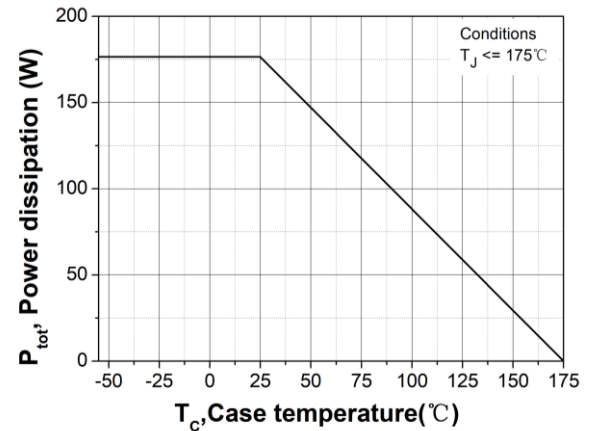


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

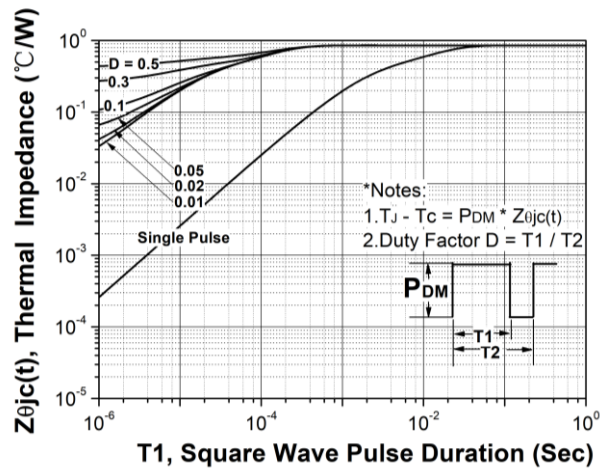


Figure 21. Transient Thermal Impedance (Junction - Case)

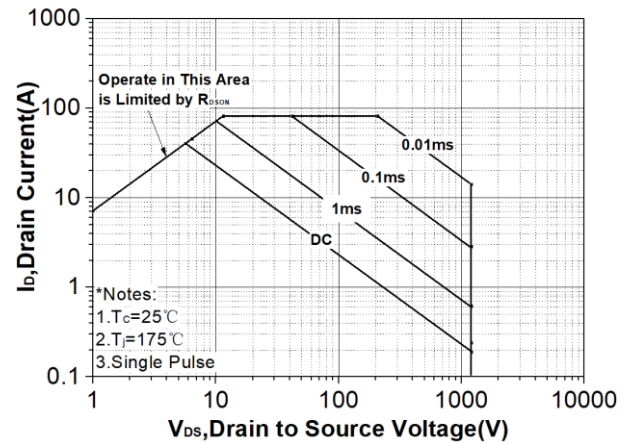


Figure 22. Safe Operating Area

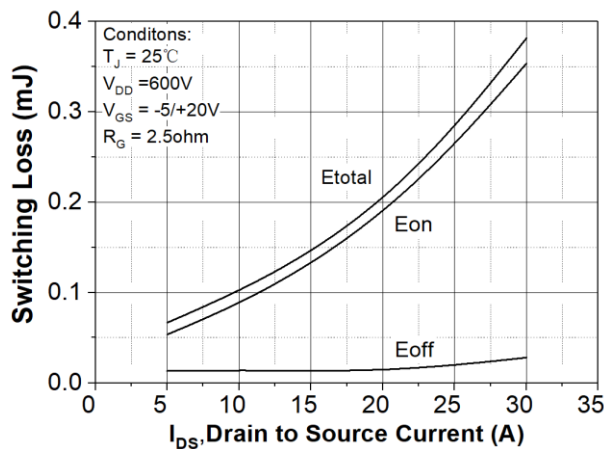


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600V$)

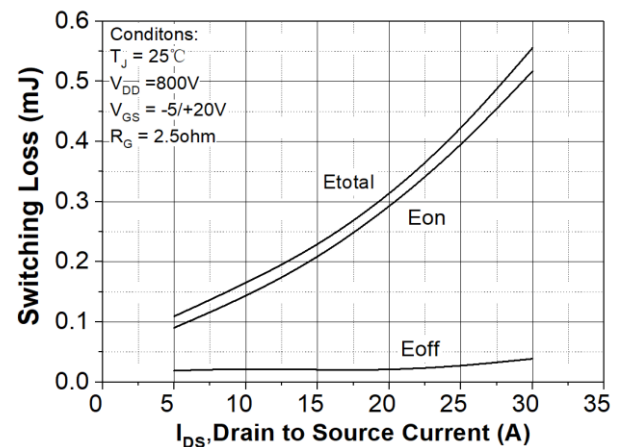


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800V$)

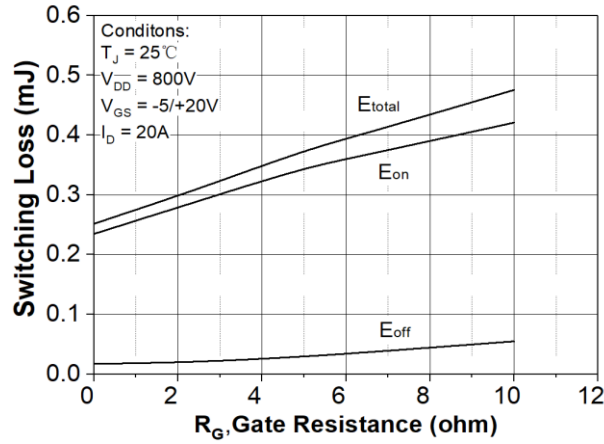


Figure 25. Clamped Inductive Switching Energy vs. $R_{G(\text{ext})}$

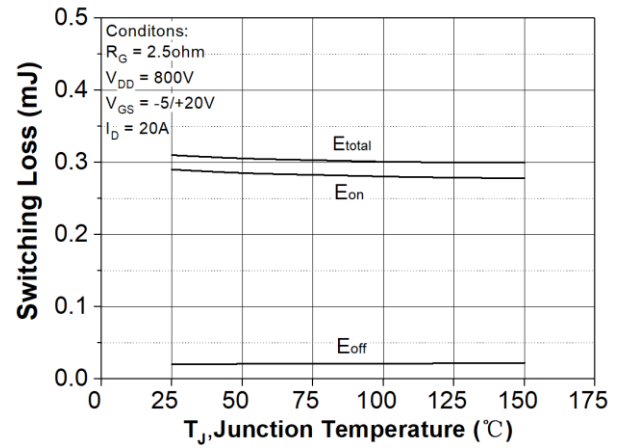


Figure 26. Clamped Inductive Switching Energy vs. Temperature

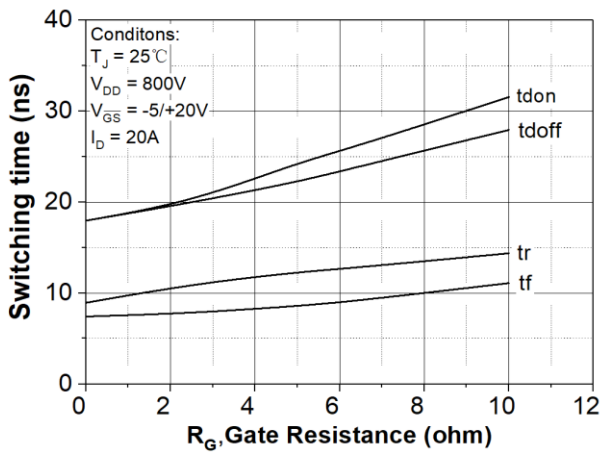


Figure 27. Switching Times vs. $R_{G(\text{ext})}$

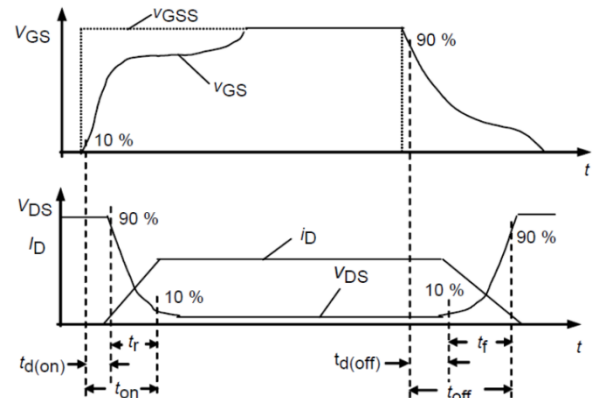
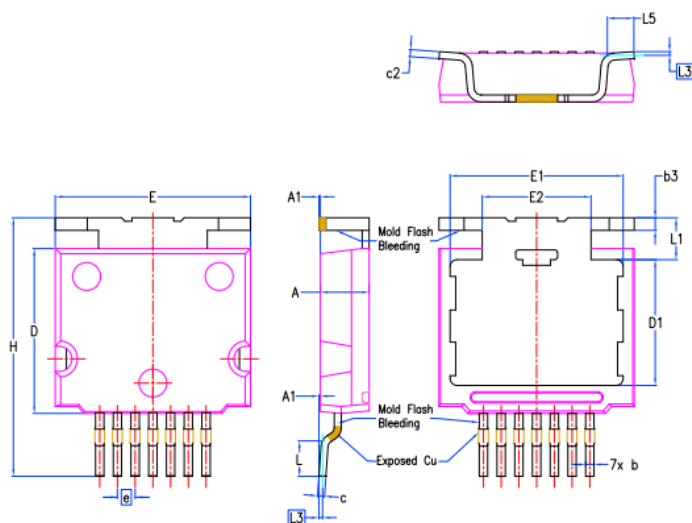


Figure 28. Switching Times Definition

Mechanical Dimensions T2PAK



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	3.40	3.50	3.60
A1	0.00	0.10	0.25
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.10
E	13.90	14.00	14.10
E1	12.30	12.40	12.50
E2	7.75	7.80	7.85
e	1.27 BSC		
H	18.00	18.50	19.00
L	2.30	2.50	2.75
L1	—	3.05	—
L3	—	0.26	—
L5	1.70	1.90	2.15

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